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To:	**Examiner Magid Y. Dimyan** **GROUP ART UNIT: 2825** **Confirmation No.: 2534**	**From:**	John S. Sensny, Esq.
Fax:	571-273-8300	**Pages:**	22 (including fax cover sheet)
Phone:		**Date:**	5/8/2006
Re:	See Below	**CC:**	

AMENDMENT UNDER 37. C.F.R. §1.111

● **Comments:**

1.   Amendment Transmittal Letter (in duplicate);
2.   Amendment under 37 C.F.R. §1.111;
3.   Authorization to Charge IBM (Yorktown) Deposit Account No. 50-0510/IBM in the amount of $200.00 for the extra independent claim fee; and
4.   Certificate of Transmission by Facsimile dated May 8, 2006.

Applicants: Harold M. Chase, et al.
Serial No.:   10/663,020
Filed: September 16, 2003
For: METHOD FOR VLSI SYSTEM DEBUGAND TIMING ANALYSIS
Docket: YOR919980343US2 (12675Z)

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CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)	Docket No.
Applicant(s): Harold M. Chase, et al.	YOR919980343US2 (12675Z)

Application No. 10/663,020	Filing Date September 16, 2003	Examiner Magid Y. Dimyan	Group Art Unit 2825

Invention: **METHOD FOR VLSI SYSTEM DEBUG AND TIMING ANALYSIS**

CONFIRMATION NO.: 2534

I hereby certify that this _____ <u>**AMENDMENT UNDER 37 C.F.R. §1.111**</u>_____
<div align="center"><i>(Identify type of correspondence)</i></div>

is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. (571) 273-8300 ___

on _____**May 8, 2006**_____
<div align="center"><i>(Date)</i></div>

<div align="center">

John S. Sensny
_____
(Typed or Printed Name of Person Signing Certificate)

John S Sensny
_____
(Signature)

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<div align="center">Note: Each paper must have its own certificate of mailing.</div>

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AMENDMENT TRANSMITTAL LETTER (Large Entity)	Docket No.
Applicant(s): Harold M. Chase, et al.	YOR919980343US2 (12675Z)

Application No. 10/663,020	Filing Date September 16, 2003	Examiner Magid Y. Dimyan	Customer No. 23389	Group Art Unit 2825	Confirmation No. 2534

Invention: METHOD FOR VLSI SYSTEM DEBUG AND TIMING ANALYSIS

COMMISSIONER FOR PATENTS:

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED

	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	45 -	45 =	0	x $50.00	$0.00
INDEP. CLAIMS	6 -	5 =	1	x $200.00	$200.00
Multiple Dependent Claims (check if applicable) ☐					$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					$200.00

☐ No additional fee is required for amendment.

☒ Please charge Deposit Account No.   50-0510/IBM   in the amount of $200.00

☐ A check in the amount of   to cover the filing fee is enclosed.

☒ The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account   50-0510/IBM

     ☒ Any additional filing fees required under 37 C.F.R. 1.16.

     ☒ Any patent application processing fees under 37 CFR 1.17.

☐ Payment by credit card. Form PTO-2038.

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.

John S. Sensny
Signature

Dated: May 8, 2006

John S. Sensny
Registration No. 28,757

Scully, Scott, Murphy & Presser, P.C.
400 Garden City Plaza - Suite 300
Garden City, New York 11530
(516) 742-4343

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on

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CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)	Docket No.
Applicant(s): Harold M. Chase, et al.	YOR919980343US2 (12675Z)

Application No. 10/663,020	Filing Date September 16, 2003	Examiner Magid Y. Dimyan	Group Art Unit 2825

Invention: **METHOD FOR VLSI SYSTEM DEBUG AND TIMING ANALYSIS**

CONFIRMATION NO.: 2534

I hereby certify that this _____ **AMENDMENT UNDER 37 C.F.R. §1.111** _____
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Harold M. Chase, et al.   **Examiner:** Magid Y. Dimyan

Serial No.: 10/663,020   **Art Unit:** 2825

Filed: September 16, 2003   **Docket:** YOR91998034US2 (12675Z)

For: METHOD FOR VLSI SYSTEM   **Dated:** May 8, 2006
       DEBUG AND TIMING ANALYSIS

Confirmation No.: 2534

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT UNDER 37 C.F.R. §1.111

Sir:

In response to the Office Action dated February 6, 2006, please amend the subject
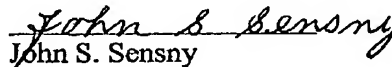
patent application as follows.

Amendments to the Specification are reflected on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims, which begins on

page 3 of this paper.

Remarks begin on page 13 of this paper.

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this paper is being facsimile transmitted to the Patent and
Trademark Office on the date shown below.

Dated: <u>May 8, 2006</u>   *John S. Sensny*
                                 John S. Sensny

05/09/2006 AWDNDAF1 00000017 500510   10663020

01 FC:1201   200.00 DA